

Top plan view of a semiconductor device 20. The device includes a central region 46, a top edge 22, and a bottom edge 28. A series of rectangular blocks 30 and 32 are arranged in a row. Arrows 38 indicate a specific direction. A dashed line 1B is shown, and a curved arrow 27 points to a specific feature.

Fig. 1 A

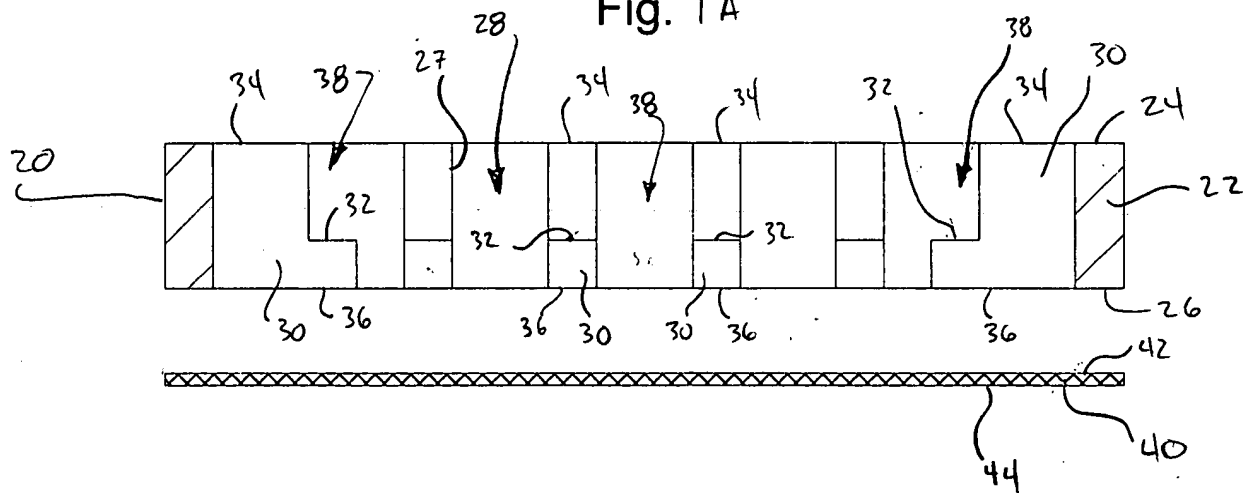


Fig. 1B

19 Feb.

FIG. 2A

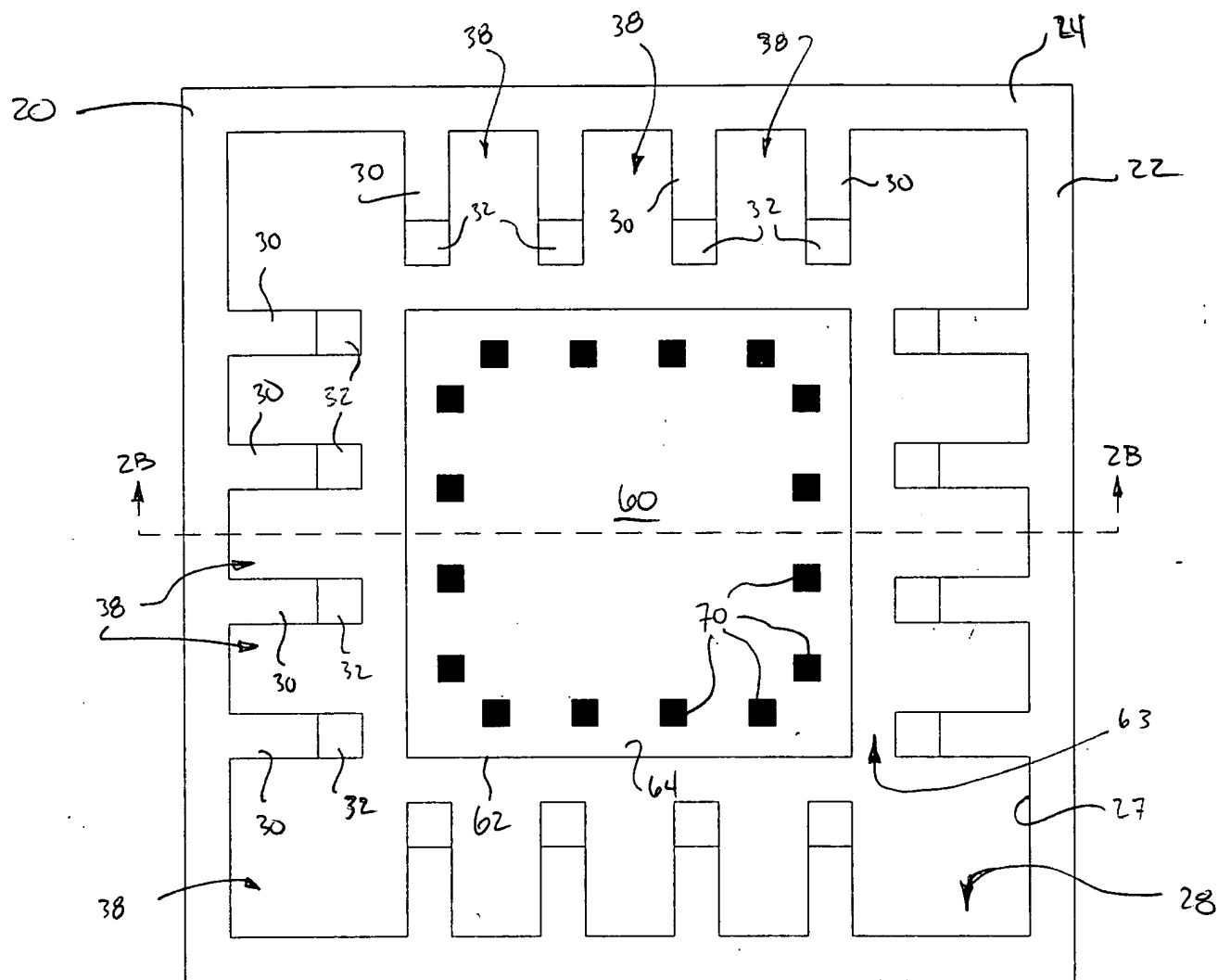


Fig. 2A

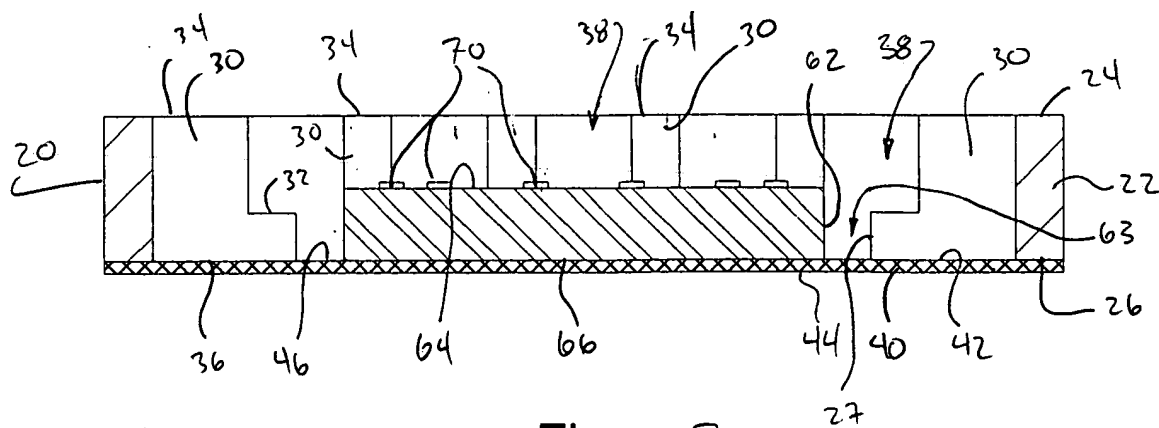


Fig. 2B

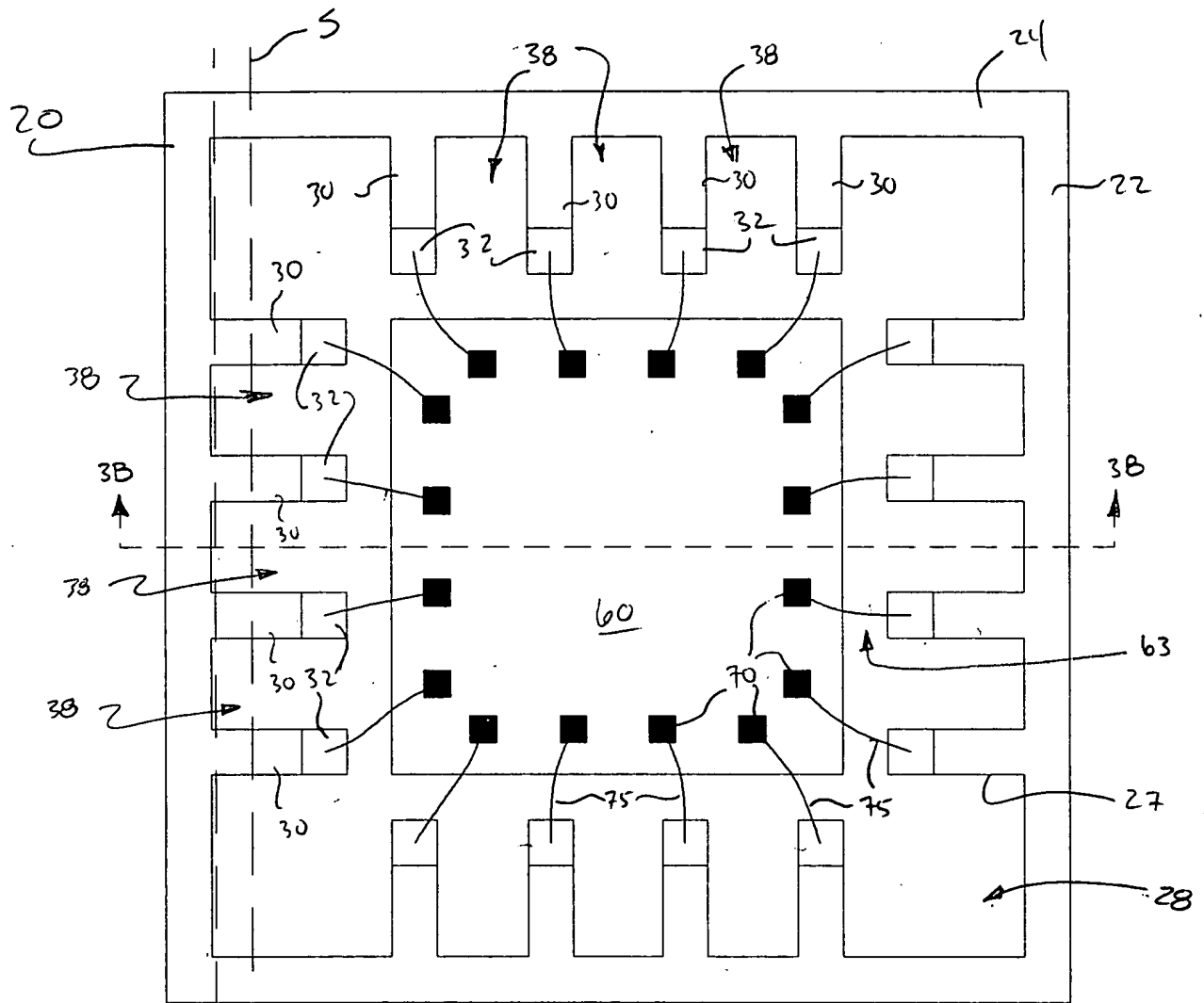


Fig. 3A

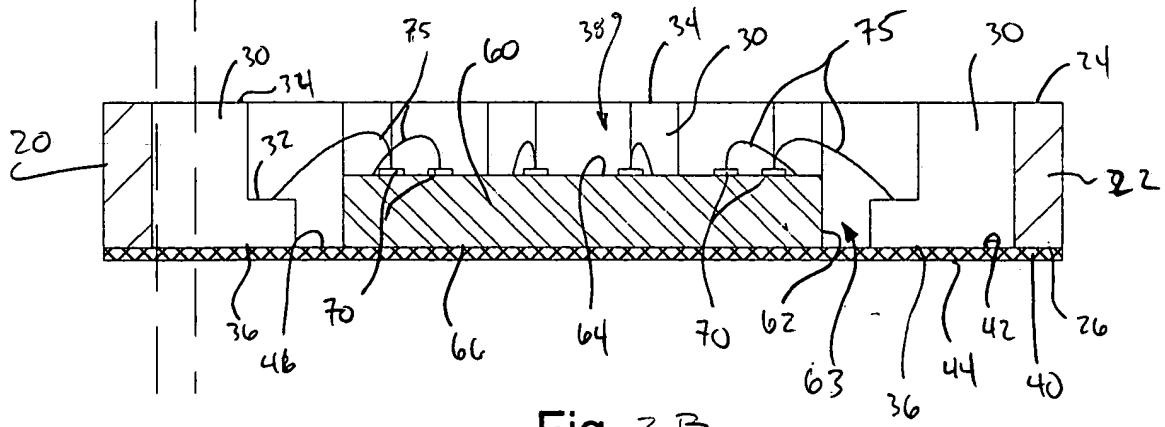
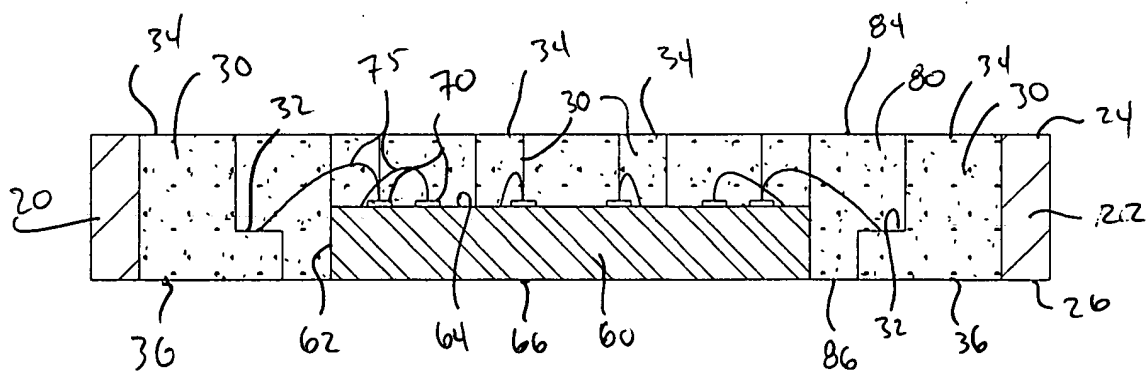
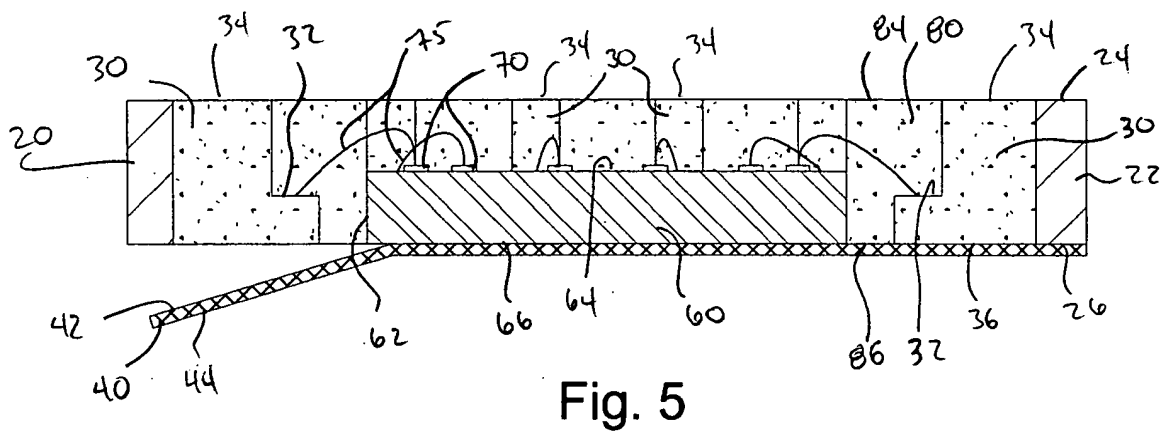
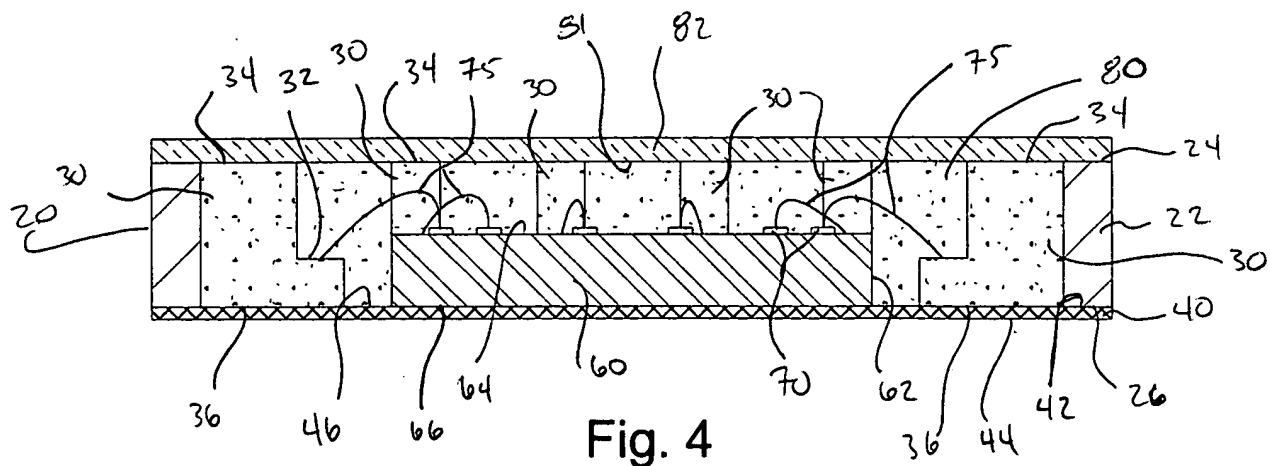


Fig. 3B



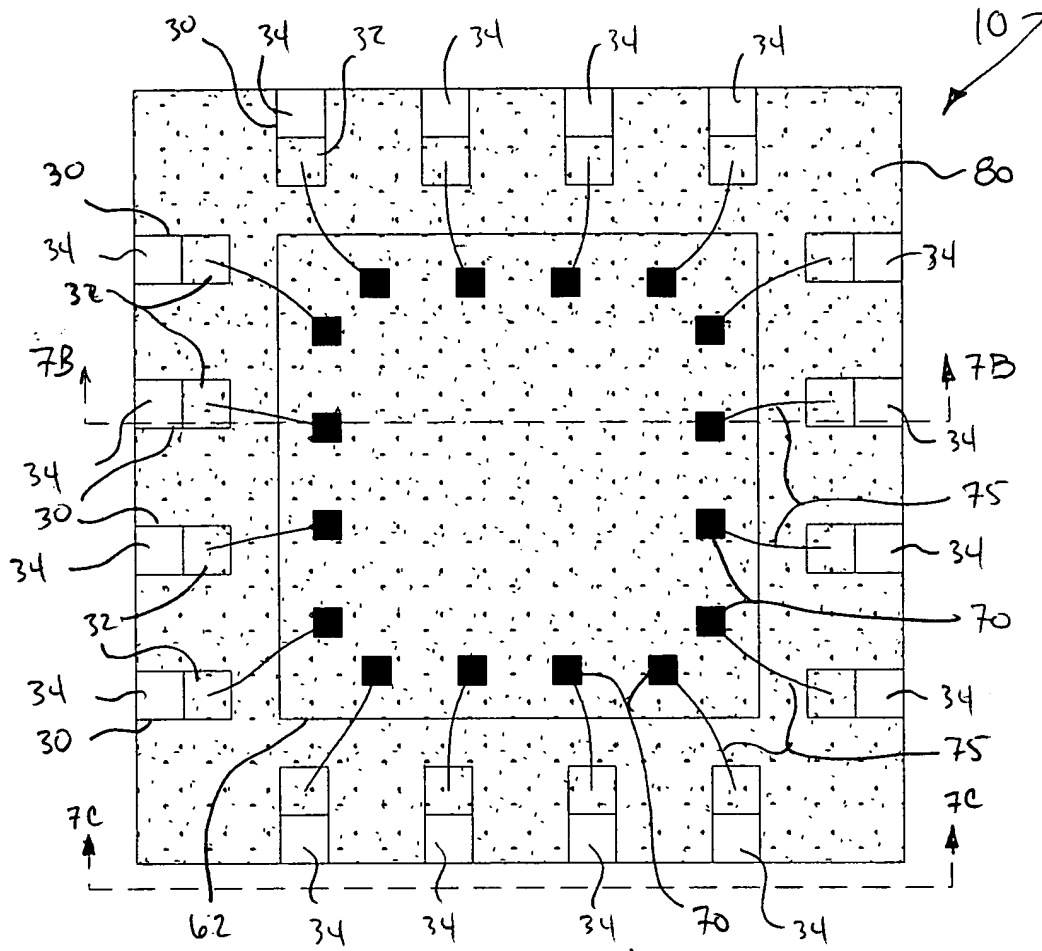


Fig. 7A

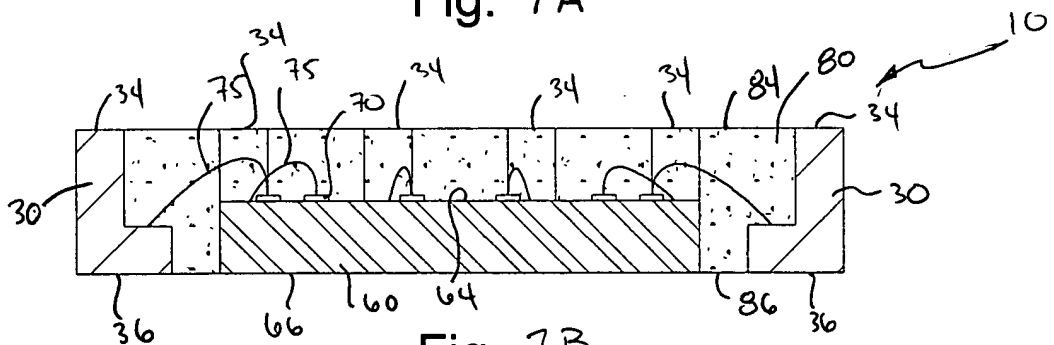


Fig. 7B

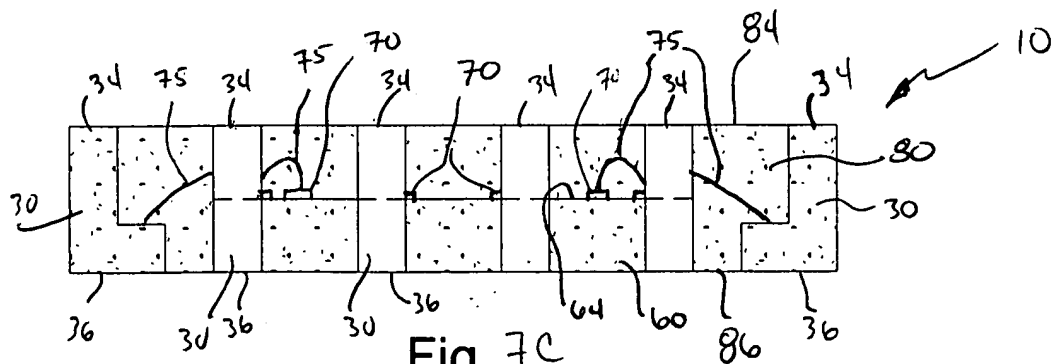


Fig. 7C

FIG. 8

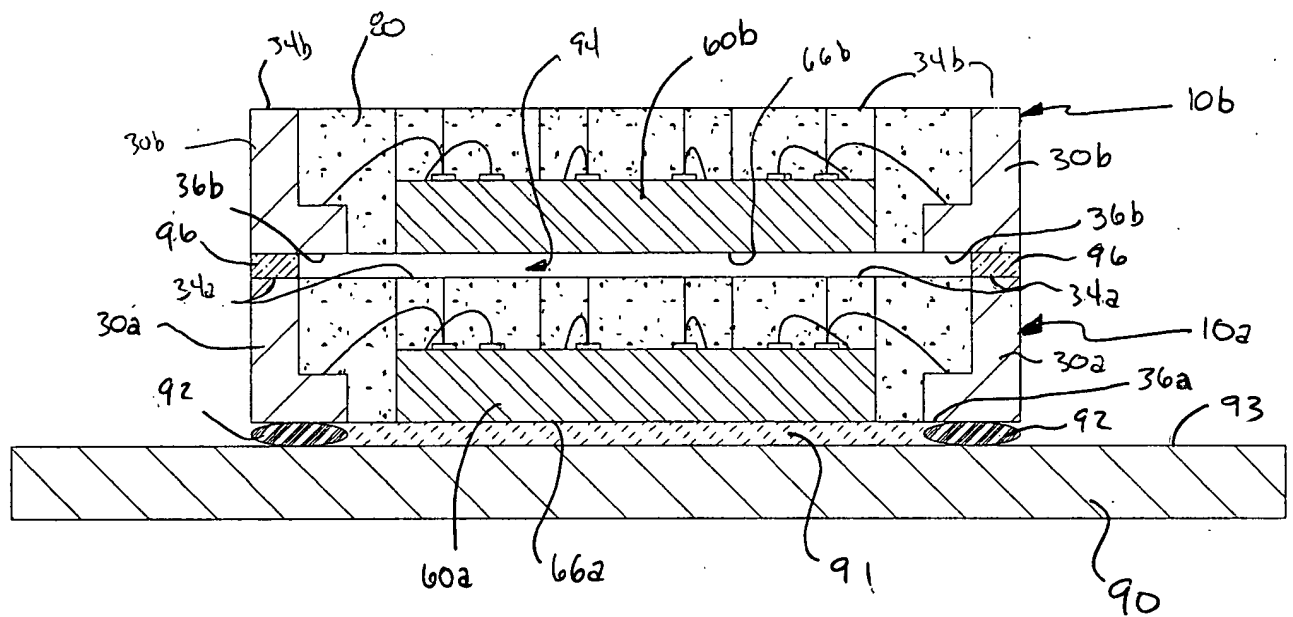


Fig. 8

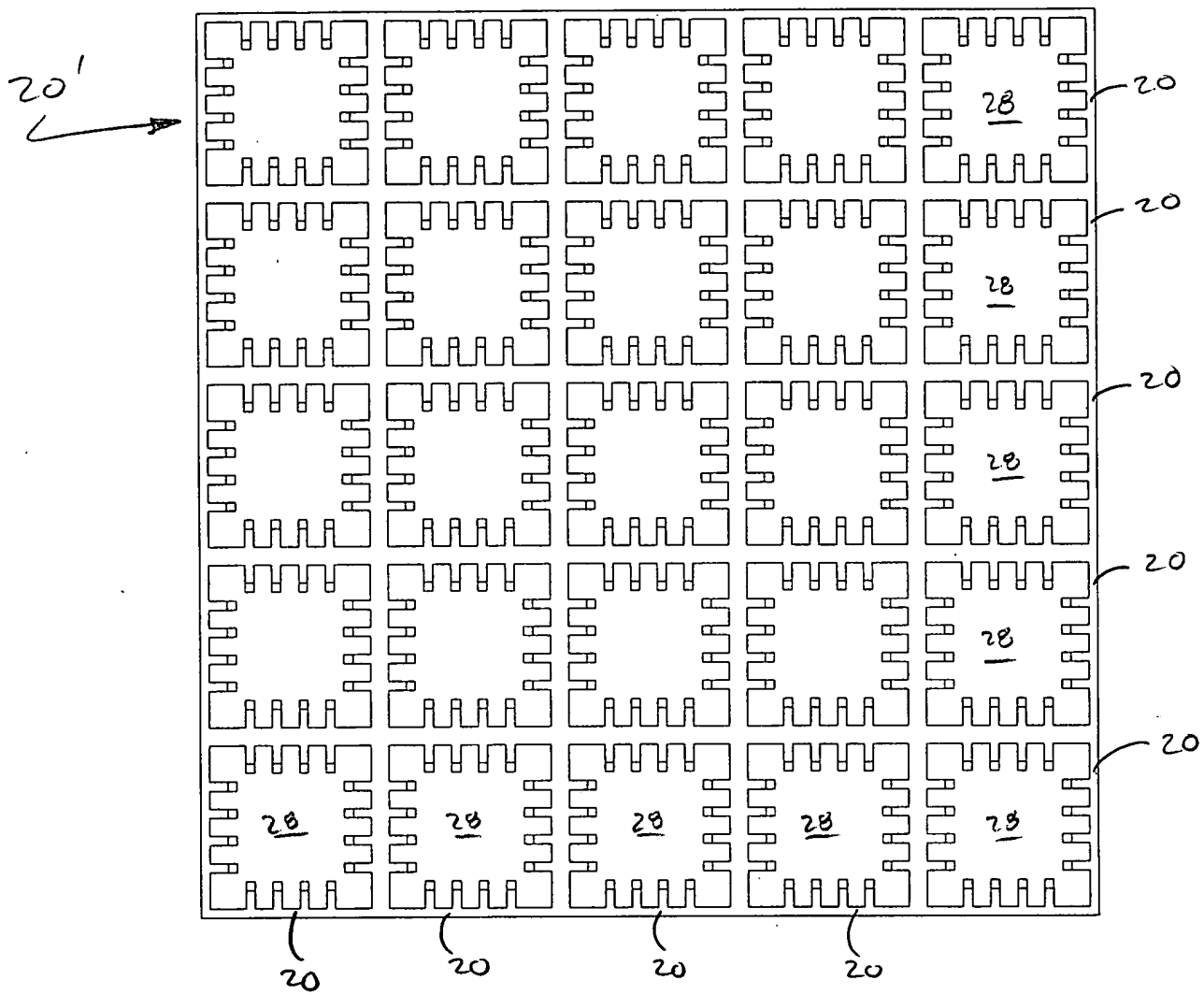


Fig. 9

FIG. 10A

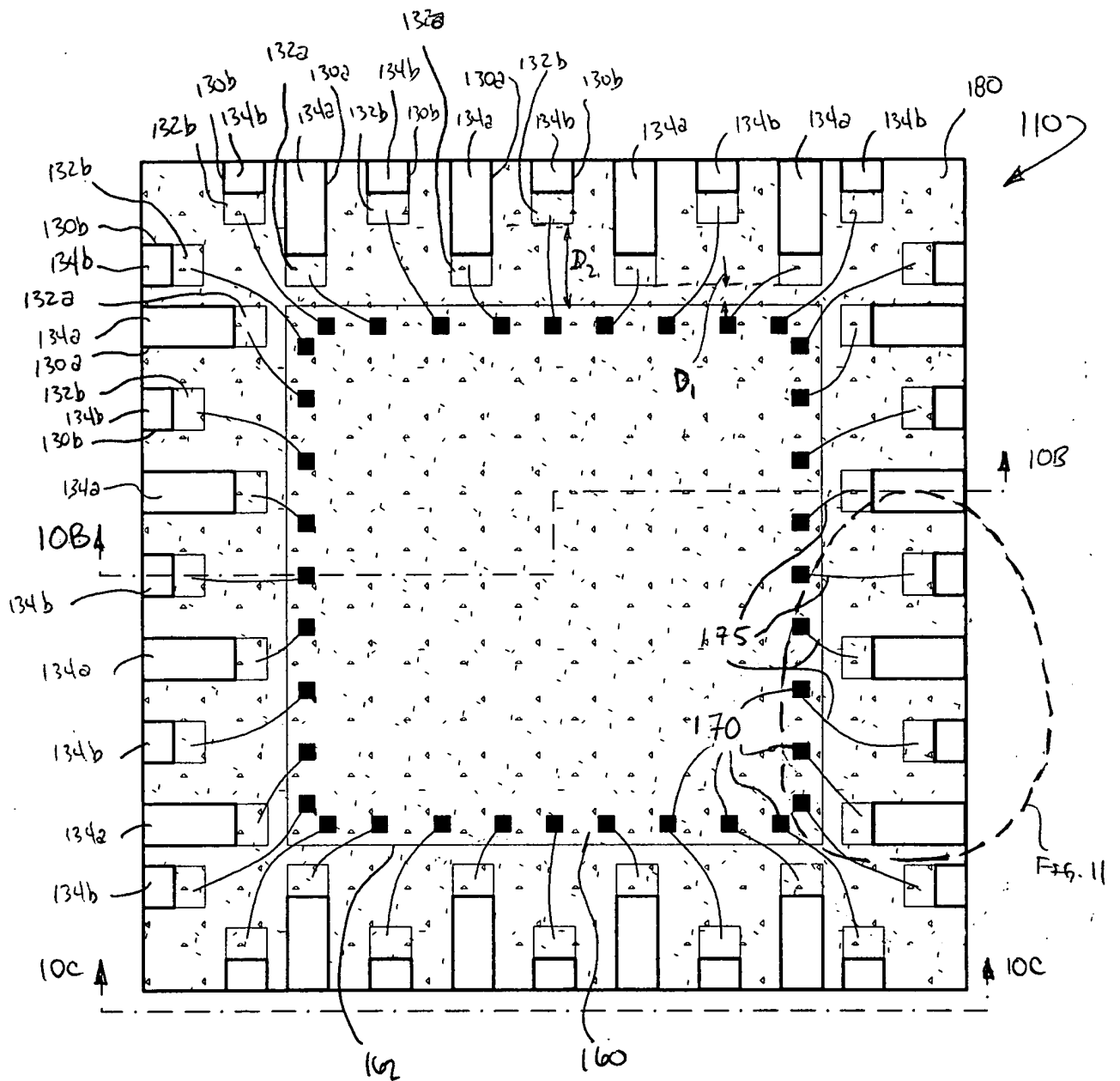


Fig. 10A



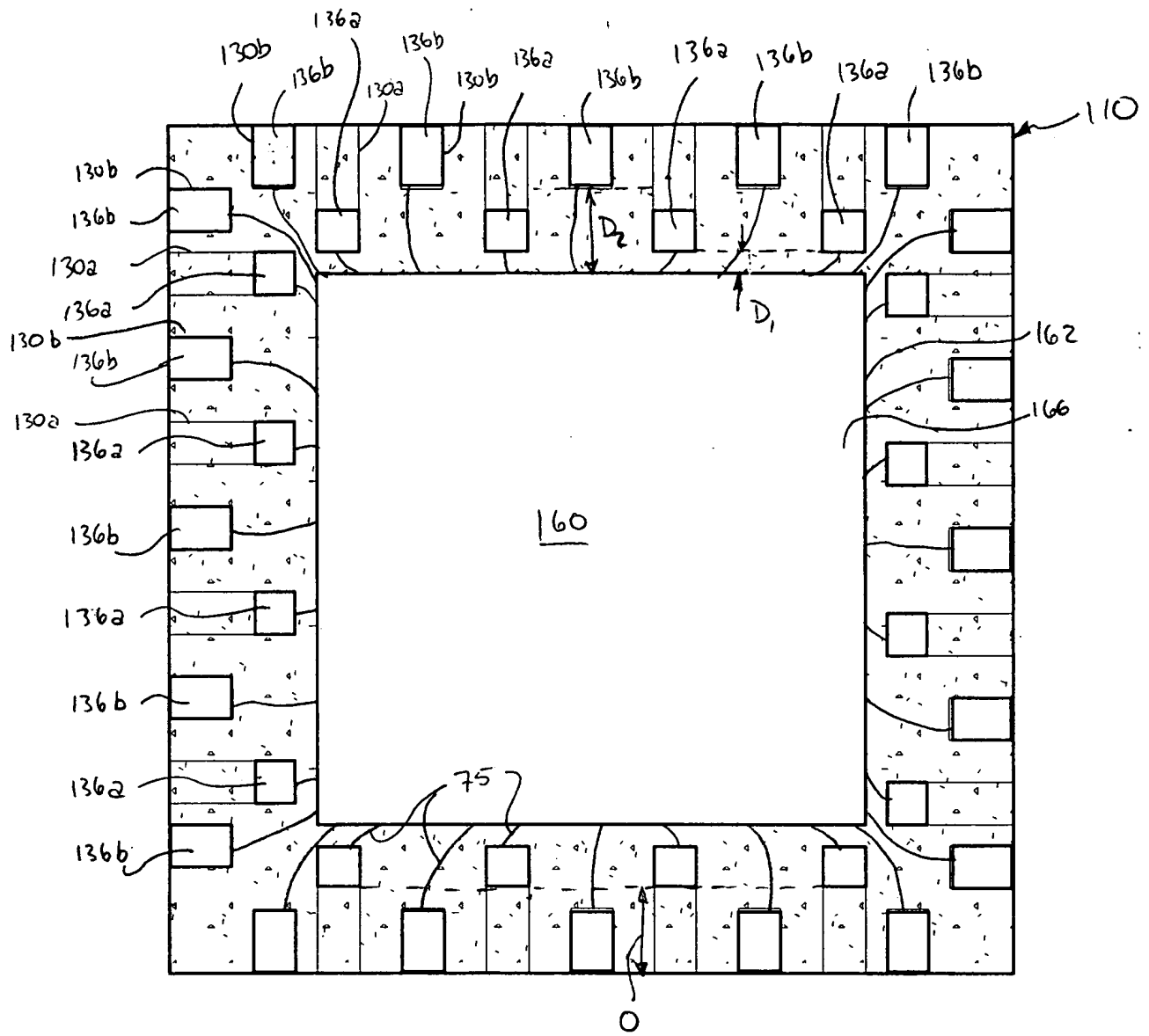


Fig. 10D

[illegible]